OSEK/VDX

OSEK Run Time Interface (ORTI)

Appendix B0: Processor Register Naming

Version 1.0

29. October 2002
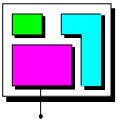
Preface

OSEK/VDX is a joint project of the automotive industry. It aims at an industry standard for an open-ended architecture for distributed control units in vehicles.

For detailed information about OSEK project goals and partners, please refer to the "OSEK Binding Specification".

General conventions, explanations of terms and abbreviations have been compiled in the additional inter-project "OSEK Overall Glossary".

Regarding implementation and system generation aspects please refer to the "OSEK Implementation Language" (OIL) specification.
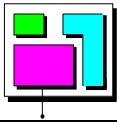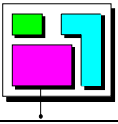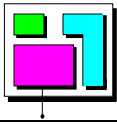
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1 Introduction

This document describes the CPU specific register names reserved for the use with ORTI CONTEXT object. For each supported processor there is a separate appendix named B1, B2, ...

2 Rules

The following rules must be obeyed with respect to these appendices:

1. Any reference to a specific CPU register must use the name defined in the appropriate appendix,

2. Register names must consist of CPU name and register name, e.g.:
    for ARM7: _ARM7_R0, _ARM7_R1, ...
    for PowerPC: _PPC_R0, PCC_R1, ...
    for HC12: _HC12_D, _HC12_X, _HC12_PC, ...

3. A vendor is permitted to add vendor-specific registers, however the names used must not conflict with those in the appropriate appendix,

4. It is not necessary to provide definitions in the ORTI for every register specified in the appropriate appendix.
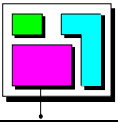
3 Adding new appendices

1. Proposals can be posted by anyone to the mailing list of the Debug Interface Working Group.

2. If the proposal is accepted by the Debug Interface Working Group, a release candidate of the proposal will be published at the OSEK web site.

3. After appropriate review period, the appendix may be released by the Debug Interface Working Group.
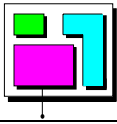
4 History

Version	Date	Remarks
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